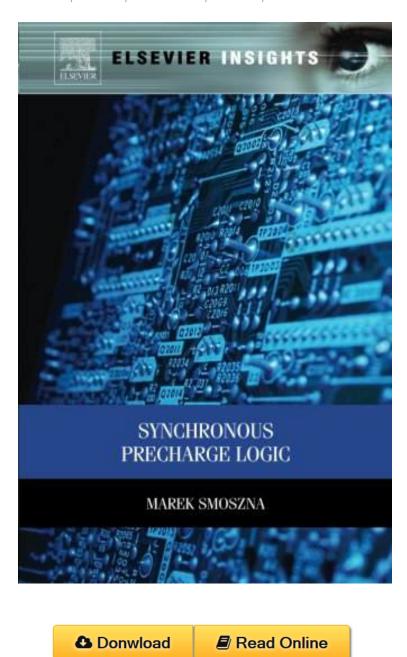
## **Synchronous Precharge Logic (Elsevier Insights)**

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**By Marek Smoszna : Synchronous Precharge Logic (Elsevier Insights)** Synchronous Precharge Logic (Elsevier Insights):

Precharge logic is used by a variety of industries in applications where processor speed is the primary goal such as VLSI very large systems integration applications Also called dynamic logic this type of design uses a clock to synchronize instructions in circuits This comprehensive book covers the challenges faced by designers when using this logic style including logic basics timing noise considerations alternative topologies and more In addition advanced About the Author Marek Smoszna is a memory design engineer at NVIDIA Corporation He was born in Poland in 1972 He received his BS and MS in electrical engineering from Rensselaer Polytechnic Institute in 1995 and 1996 respectively He holds two patents in the

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